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Ross

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(54) **REFERENCE VOLTAGE GENERATOR**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 307 days.

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(52) **U.S. Cl.** **365/226**; 365/189.09

(58) **Field of Classification Search** 365/226, 365/189.09, 230.03

See application file for complete search history.

(57) **ABSTRACT**

Voltage differences between memory module power sources and memory controller power sources are corrected for when interpreting data signals passed between memory modules and a controller. Correction is provided by the reference voltage generated from the memory module or controller power source voltage so that if the voltage amplitude of a data signal transmitted by the memory module or controller varies due to changes in the power source voltage, the reference signal voltage amplitude will correspondingly vary. Thus, the data signal receiving controller or memory module can use the reference signal to properly interpret the voltage amplitude of data received.

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