



US005848288A

United States Patent [19] O'Connor

[11] Patent Number: **5,848,288**
[45] Date of Patent: **Dec. 8, 1998**

- [54] **METHOD AND APPARATUS FOR ACCOMMODATING DIFFERENT ISSUE WIDTH IMPLEMENTATIONS OF VLIW ARCHITECTURES**
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- [21] Appl. No.: **530,076**
- [22] Filed: **Sep. 20, 1995**
- [51] Int. Cl.⁶ **G06F 9/30**
- [52] U.S. Cl. **395/800.24; 395/379; 395/386**
- [58] Field of Search **395/379, 382, 395/386, 391, 800.24**

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[57] ABSTRACT

A method and apparatus which permits a computer system to execute variable size instruction bundles. A processor fetches an instruction issue group of the size it can issue in one cycle. By detecting if an end of bundle exists in an instruction issue group and disabling the issue of instruction following an end of bundle, the computer is enabled to execute code compiled for arbitrary bundle size.

16 Claims, 4 Drawing Sheets

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