US005835784A

United States Patent [19]

Gillespie et al.

[11] Patent Number: 5,835,784

[45] Date of Patent: Nov. 10, 1998

[54] **SYSTEM FOR BOOTING PROCESSOR FROM REMOTE MEMORY BY PREVENTING HOST PROCESSOR FROM CONFIGURING AN ENVIRONMENT OF PROCESSOR WHILE CONFIGURING AN INTERFACE UNIT BETWEEN PROCESSOR AND REMOTE MEMORY**

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[21] Appl. No.: **611,802**

[22] Filed: **Mar. 6, 1996**

Related U.S. Application Data

[63] Continuation-in-part of Ser. No. 490,778, Jun. 15, 1995, Pat. No. 5,696,949.

[51] **Int. Cl.**[6] **G06F 15/177**

[52] **U.S. Cl.** **395/830**; 395/652

[58] **Field of Search** 395/652, 282, 395/842, 822, 651, 280, 830; 711/206, 143; 365/230.02

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[57] **ABSTRACT**

A method and system for booting a first processor from a remote memory. In response to a reset signal, a processor which has no associated local memory is prevented from executing code and particularly its boot sequence. Because the first processor is prevented from initializing its environment, configuration cycles from a host processor should be prevented from configuring that environment until the first processor has booted. By preventing the host processor from configuring, the first processor environment's integrity is protected. Because the first processor has no local memory, address cycles generated to access local memory would normally go unclaimed on a local bus. An interface between the local bus and the remote memory is configured to claim the local memory address range from the local bus. Once the first processor is enabled, the local memory addresses are used to access the remote memory to return the necessary boot code.

14 Claims, 4 Drawing Sheets